Claims:

1. A method of compensating for output error in a sigma delta circuit, comprising:

receiving an input signal;

adding a first error voltage value, which is derived from an output signal, to the input signal;

subtracting a second error value, which is derived from the adding of a first error voltage value, to the input signal from the input signal; and

outputting an output signal result from the sigma delta circuit.

2. A sigma delta digital circuit configured to compensate for output error, comprising:

an input for receiving an input signal;

an output configured to output a output signal;

a summation component configured to add a first error voltage value, which is derived from an output signal, to an incoming input signal; and

a subtraction component configured to subtract a second error voltage value, where the second error voltage value is derived from the adding of a first error voltage value to an incoming input signal.

3. A sigma delta digital circuit according to Claim 2, further comprising a filter configured to filter an input signal according to a filter function, wherein the filter generates noise that distorts the filtered input signal, wherein the distortion results in the first error value.

4. A sigma delta digital circuit according to Claim 2, further comprising a filter configured to filter an input signal according to a filter function, wherein the filter generates noise that distorts the filtered input signal, wherein the distortion results in the second error value.